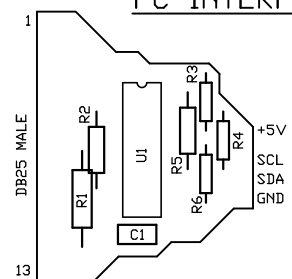


FIG 9 COMPONENT LAYOUT

PC INTERFACE



PC2I2C Top Overlay